

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|--------------------------------|------------------|---------|------------------|
| L1 | 282 | ((low adj temperature adj polysilicon) or LTPS) and ((thin adj film adj transistor) or TFT) and gate and substrate and source and drain | USPAT; EPO; JPO; DERWENT | OR | OFF | 2005/09/19 15:23 |